

Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Page 1 of 2	Application No.	09/993,138
	Filed	11/16/2001
	First Inventor	Zerbe, Jared L.
	Art Unit	2614
	Examiner	SINGH, Ramnandan P
	Atty. Docket No.	RBS2.P043

U.S. Patent Documents					
Examiner Initials*	US Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication	Relevant Pages, Columns, Lines
	Number	Kind			
	2003-0095606		HOROWITZ, Mark	5/22/2003	
	5740201		Hui, David Tinsun	4/14/1998	
	6067594		PERINO, Donald	5/23/2000	
	6137455		Duo, Rung-Fu	10/24/2000	
	6154498		Dabral et al.	11/28/2000	
	6278740		Nordyke, Keith D.	8/21/2001	
	6359931		PERINO, Donald	3/19/2002	
	6556628		Poulton et al.	4/29/2003	

Non Patent Literature Documents		
Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Translation
	CARUSONE ET AL., "Differential Signaling with a Reduced Number of Signal Paths", IEEE Transactions on Circuits and Systems - II: Analog and Digital Signal Processing, March 2001, pgs 294-300, Vol. 48, No. 3	
	MARSLAND ET AL., "A Network Multi-Processor for Experiments in Parallelism", March 31, 1991, pgs 1-29	
	STAN, MIRCEA and BURLESON, W.P., "Bus-Invert Coding for Low Power I/O", IEEE Transactions on Very Large Scale Integration Systems, Vol. XX, No. Y, 1999	

Examiner Signature	/Alexander Jamal/	Date Considered	05/06/2008
---------------------------	-------------------	------------------------	------------

* Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /AJ/

Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Page 2 of 2	Application No.	09/993,138
	Filed	11/16/2001
	First Inventor	Zerbe, Jared L.
	Art Unit	2614
	Examiner	SINGH, Ramnandan P
	Atty. Docket No.	RBS2.P043

Non Patent Literature Documents		
Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Trans- lation
	STAN, MIRCEA and BURLESON, W.P., "Coding a Terminated Bus for Low Power", Great Lakes Symposium on VLSI , pp 70 -73, March 1995	
	WIDMER, A.X. and FRANASZEK, P.A., "A DC-Balanced, Partitioned-Block, 8B/10B Transmission Code," IBM J. Res. Develop., Vol. 27, No. 5, September 1983, pp. 440-451.	

Examiner Signature	/Alexander Jamal/	Date Considered	05/06/2008
-------------------------------	-------------------	----------------------------	------------

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /AJ/